

## Qam Modulator Fpga Ip Core Iprium

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### **Qam Modulator Fpga Ip Core**

QAM is a signal in which two carriers shifted in phase by 90 degrees are modulated and the resultant output consists of both amplitude and phase variations. These IP cores can meet High speed data rate applications on FPGA's UTS offers IPs under flexible licensing models, to meet requirements of different category of users.

### **UTS - QAM Modulator and Demodulator IP core**

Modulators / Demodulators. The IP Core implements the QAM modulation with filtering and signal interpolation. Mapper Pulse Shaping Filter Resampler Quadrature Modulator NCO DDS  
Version : 4.0 Build date : 2014.09 Ordering code : ip-qam-modulator Supported technologies : FPGA (Xilinx, Intel/Altera, Lattice, Microsemi/Actel), ASIC (Digital ASIC)

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## **QAM Modulator IP Core - FPGA IP Cores, wireless modems, FEC**

QAM Modulator IP Core is a sample 32 bandwidth control (symbol rate): 0.01% to 25% of `iclk` output at baseband (I channel) or at an intermediate frequency `odatq` `W_DAC` modulator output at baseband (Q channel) ready to accept input data

## **QAM Modulator FPGA IP Core - IPrium**

The QAM IP core is available in different configurations. Depending on your budget and wishes, finished binary images or the complete VHDL source code can be delivered - please contact us for a quote for your planned application.

## **QAM Modulator FPGA IP-Core - maintech**

Excellent MER and shoulder attenuation values are key

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advantages of maintech's multiple FPGA QAM modulation with DUC. Short-term evaluation platforms: Xilinx Virtex-6 LX130T Evaluation Kit by AVNET Analog Devices AD9739-CMTS-EBZ Evaluation Kit (see fig.) IP Core.

### **16ch QAM & DUC FPGA IP-Core - Jordaan Electronics**

The DVB-C J.83A/C cable modulator modulates an MPEG-TS DVB-SPI input into a QAM-16/32/65/128/256 output in base band I/Q. Description The MVD DVB-C Modulator J.83 Annex A/C core is a drop-in module for FPGA that includes the following functions:

### **QAM modulator - DVB-C J.83 Annex A/C modulator - IP core ...**

The J.83B cable modulator modulates an MPEG-TS DVB-SPI input into a QAM-16/256 output in baseband I/Q. Description The MVD Cable modulator J83B core is a drop-in module for FPGA that includes the following functions:

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## **QAM modulator - J.83 Annex B - IP core for FPGA**

The IP Core contains Scrambler, RS Encoder, Trellis Encoder, 64-QAM/256-QAM Modulator. 21 IP Provider : Give the best exposure to your IPs, by listing your products for free in the world's largest Silicon IP catalog (6 500 products from more than 400 companies )

## **Quadrature Amplitude Modulation IP Core - design-reuse.com**

High Performance VSB/QAM Demodulator Core Full digital demodulator supports 8-VSB and 64/256-QAM. Fully separable core for optional OOB forward data channel. Prototyped in FPGA and tested in the lab and in the field by independent...

## **Xilinx Modulation/Demodulation IP Core - Design And Reuse**

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FPGA-based platforms implementing these IP cores are also available. License ... Complete VHDL/IP Core license agreement; MODULATORS-ONLY. Part number Description Specifications Price Check availability ... 67KB: \$750 : COM-1402SOFT: PSK/QAM/PSK modulator Symbol rate up to fclk/4: 117KB: \$500 : COM-1827SOFT CPM MOD: CPM modulator FSK,MSK,GFSK ...

### **FPGA/VHDL/IP cores**

IP Core Description IP Core Operation Description For correct recovery of QAM-modulated signal at the receiver must be known with an accuracy of carrier phase and symbol rate. To adjust the carrier most advanced QAM demodulators use different versions of the classical scheme Costas, and to adjust the clock speed - the scheme Gardner.

### **QAM Demodulator FPGA IP Core - modemica.com**

Versatile Digital QAM Modulator Combined with enhanced DSP

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blocks, the logic structure of Altera's Stratix® series and Arria® series FPGAs offer a flexible way to implement the key elements of the Quadrature Amplitude Modulation (QAM) signal-processing path in any region.

### **Versatile Digital QAM Modulator - Intel**

The QAM Modulator IP Core is available immediately in synthesizable Verilog or optimized netlist format, along with synthesis scripts, simulation test bench with expected results, and user manual. For further information, product evaluation, or pricing, please visit the IP Core page: [QAM Modulator IP Core](#)

### **IPrium adds 128-APSK and 256-APSK support to QAM Modulator ...**

The Creonic DVB-S2X modulator is a low-complexity high-performance solution that allows for symbol rates of up to 250 MSymb/s (2 Gbit/s for 256-APSK) on state-of-the-art FPGAs. The

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IP core performs all tasks of the inner transmitter and complements the Creonic DVB-S2X receiver solutions (DVB-S2X demodulator and DVB-S2X LDPC/BCH decoder).

### **DVB-S2X Modulator - Creonic**

This video is a short demonstration of an upcoming course focusing on the design of wireless communications systems using FPGAs. This specific video covers t...

### **QAM Modulation in VHDL - Part 1 [Wireless on FPGAs Course ...**

D&R provides a directory of Quadrature Amplitude Modulation IP Core. Arm Cortex-R82: Combining high-performance 64-bit real-time and applications processing for the next generation of storage devices

### **Quadrature Amplitude Modulation IP Core**



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The CMS0009 core is intended to provide a very efficient Intel® FPGA implementation of all functions required to take the output from an MPEG-2 transport multiplexer and modulate it according to the DVB-T modulation for 2K, 4K, and 8K COFDM modes as well as hierarchical transport systems.

### **DVB-T / DVB-H Modulator**

The IP core is optimally designed to be implemented in a single FPGA chip that can interface two high-speed digital to analog converters (DAC). The generated OFDM signal consists of two independent outputs for the in-phase (I) and quadrature (Q) paths of 5 bits resolution that can be sampled at up to 16 GS/s.

### **IP Cores - Fraunhofer Heinrich Hertz Institute**

The QPSK Modulator is then simulated using Modelsim and Xilinx environment tool for FPGA design as well as implemented on a Spartan 6 LX9 FPGA. The modulator algorithm has been

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implemented on FPGA...

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